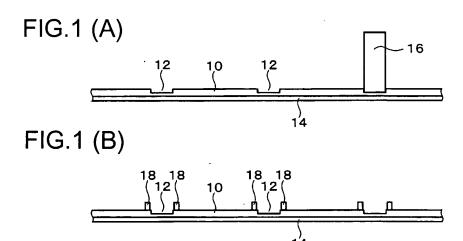
HOGAN & HARTSON – 81754.0105 Osamu OMORI, "Semiconductor Device, Method of manufacturing the Same, Circuit Substrate and Electronic Equipment" EV 324 111 242 US Page 1 of 11



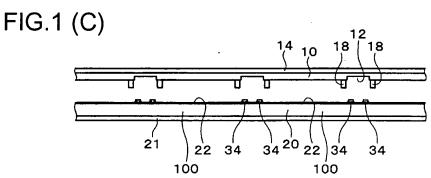
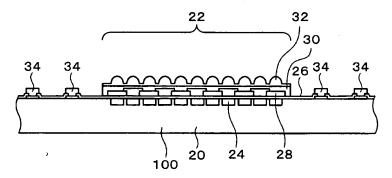
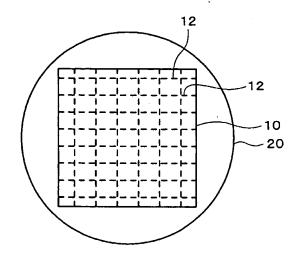


FIG.2



HOGAN & HARTSON – 81754.0105 Osamu OMORI, "Semiconductor Device, Method of manufacturing the Same, Circuit Substrate and Electronic Equipment" EV 324 111 242 US Page 2 of 11

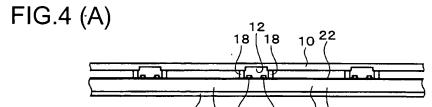
FIG.3



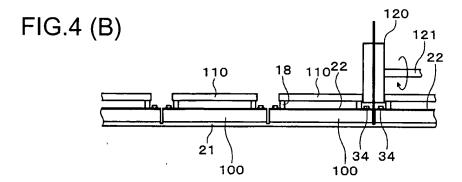
20

100

34



) 34 100



HOGAN & HARTSON – 81754.0105 Osamu OMORI, "Semiconductor Device, Method of manufacturing the Same, Circuit Substrate and Electronic Equipment" EV 324 111 242 US Page 3 of 11

FIG.5 (A)

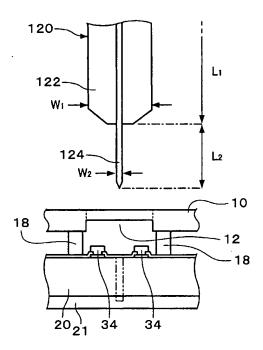
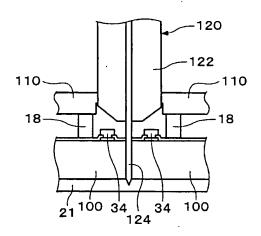
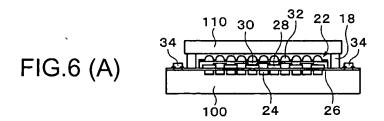
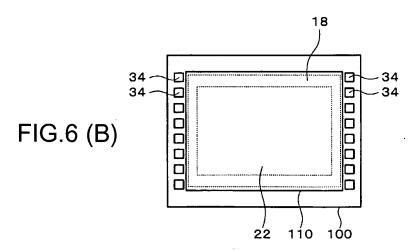


FIG.5 (B)

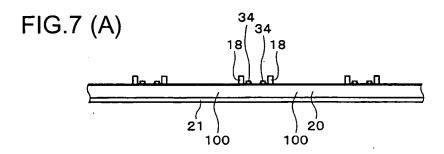


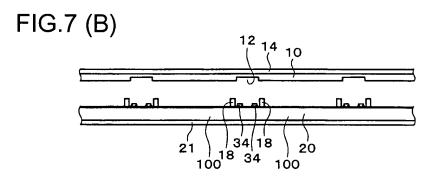
HOGAN & HARTSON – 81754.0105 Osamu OMORI, "Semiconductor Device, Method of manufacturing the Same, Circuit Substrate and Electronic Equipment" EV 324 111 242 US Page 4 of 11



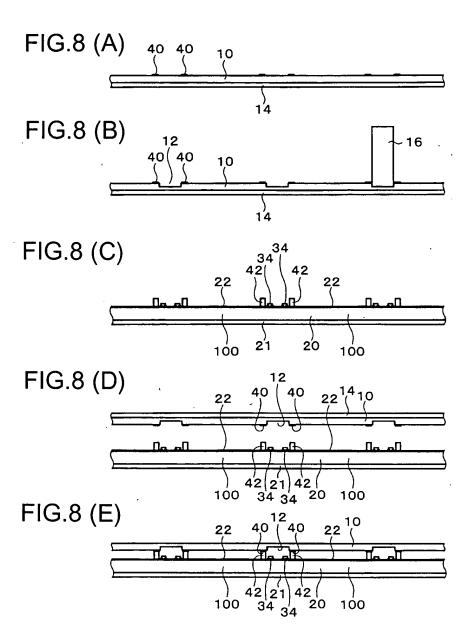


HOGAN & HARTSON – 81754.0105 Osamu OMORI, "Semiconductor Device, Method of manufacturing the Same, Circuit Substrate and Electronic Equipment" EV 324 111 242 US Page 5 of 11

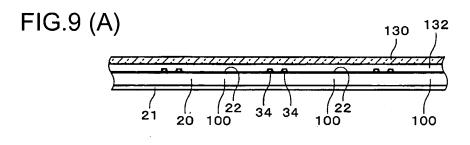


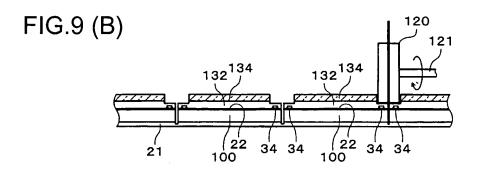


HOGAN & HARTSON – 81754.0105 Osamu OMORI, "Semiconductor Device, Method of manufacturing the Same, Circuit Substrate and Electronic Equipment" EV 324 111 242 US Page 6 of 11

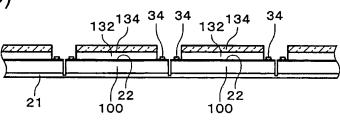


HOGAN & HARTSON – 81754.0105 Osamu OMØRI, "Semiconductor Device, Method of manufacturing the Same, Circuit Substrate and Electronic Equipment" EV 324 111 242 US Page 7 of 11









HOGAN & HARTSON – 81754.0105 Osamu OMORI, "Semiconductor Device, Method of manufacturing the Same, Circuit Substrate and Electronic Equipment" EV 324 111 242 US Page 8 of 11

FIG.10 (A)

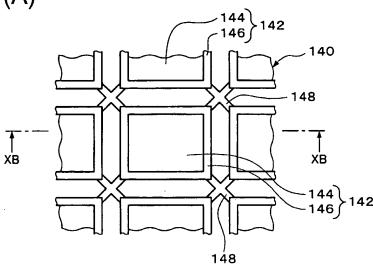


FIG.10 (B)

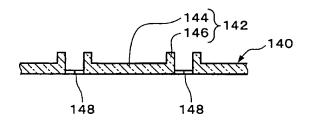
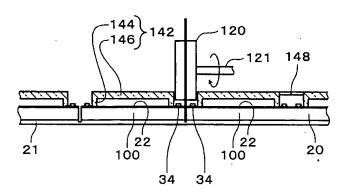


FIG.11



HOGAN & HARTSON – 81754.0105 Osamu OMORI, "Semiconductor Device, Method of manufacturing the Same, Circuit Substrate and Electronic Equipment" EV 324 111 242 US Page 9 of 11

FIG.12

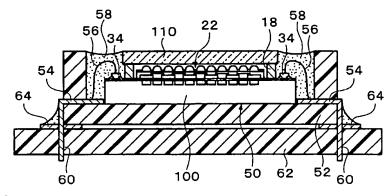


FIG.13

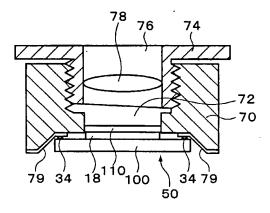
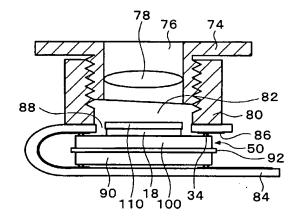


FIG.14



HOGAN & HARTSON – 81754.0105
Osamu OMORI, "Semiconductor Device, Method of manufacturing the Same, Circuit Substrate and Electronic Equipment"

EV 324 111 242 US
Page 10 of 11

FIG.15

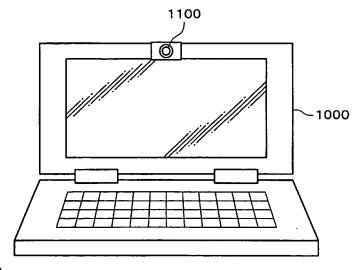
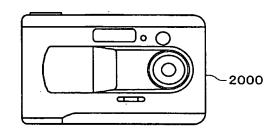


FIG.16



HOGAN & HARTSON – 81754.0105 Osamu OMORI, "Semiconductor Device, Method of manufacturing the Same, Circuit Substrate and Electronic Equipment" EV 324 111 242 US Page 11 of 11

